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Applicant : Randy Bonella et al.
Serial No. : 09/662,054
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Art Unit : 2115
Examiner : Dennis M. Butler

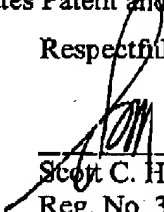
Title : DIGITAL SYSTEM OF ADJUSTING DELAYS ON CIRCUIT BOARDS

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Attached to this facsimile communication cover sheet is Response to Office Action,
faxed this 19th day of November, 2004, to the United States Patent and Trademark Office.

Respectfully submitted,

Date: November 19, 2004



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